

Appendix for SA N78_3700-3800MHz

Appendix A: Average Power Output Data for SA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
N78-3700-3800	30	20	DFT-QPSK	L	Edge_1RB_Left	19.97	PC2	PASS
N78-3700-3800	30	20	DFT-QPSK	L	Edge_1RB_Right	20.20	PC2	PASS
N78-3700-3800	30	20	DFT-QPSK	L	Outer_Full	22.52	PC2	PASS
N78-3700-3800	30	20	DFT-QPSK	L	Inner_Full	23.34	PC2	PASS
N78-3700-3800	30	20	CP-QPSK	L	Edge_1RB_Left	19.89	PC2	PASS
N78-3700-3800	30	20	CP-QPSK	L	Edge_1RB_Right	20.24	PC2	PASS
N78-3700-3800	30	20	CP-QPSK	L	Outer_Full	20.48	PC2	PASS
N78-3700-3800	30	20	CP-QPSK	L	Inner_Full	21.89	PC2	PASS
N78-3700-3800	30	20	DFT-QPSK	M	Edge_1RB_Left	21.49	PC2	PASS
N78-3700-3800	30	20	DFT-QPSK	M	Edge_1RB_Right	21.85	PC2	PASS
N78-3700-3800	30	20	DFT-QPSK	M	Outer_Full	24.15	PC2	PASS
N78-3700-3800	30	20	DFT-QPSK	M	Inner_Full	24.80	PC2	PASS
N78-3700-3800	30	20	CP-QPSK	M	Edge_1RB_Left	21.58	PC2	PASS
N78-3700-3800	30	20	CP-QPSK	M	Edge_1RB_Right	21.81	PC2	PASS
N78-3700-3800	30	20	CP-QPSK	M	Outer_Full	21.95	PC2	PASS

-3800								
N78-3700 -3800	30	20	CP-QPSK	M	Inner_Full	23.26	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	H	Edge_1RB_Left	21.83	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	H	Edge_1RB_Right	21.75	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	H	Outer_Full	24.32	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	H	Inner_Full	24.73	PC2	PASS
N78-3700 -3800	30	20	CP-QPSK	H	Edge_1RB_Left	21.75	PC2	PASS
N78-3700 -3800	30	20	CP-QPSK	H	Edge_1RB_Right	21.59	PC2	PASS
N78-3700 -3800	30	20	CP-QPSK	H	Outer_Full	22.10	PC2	PASS
N78-3700 -3800	30	20	CP-QPSK	H	Inner_Full	23.44	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Edge_1RB_Left	20.21	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Edge_1RB_Right	21.16	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Outer_Full	21.03	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Inner_Full	21.10	PC2	PASS
N78-3700 -3800	30	40	CP-QPSK	L	Edge_1RB_Left	20.99	PC2	PASS
N78-3700 -3800	30	40	CP-QPSK	L	Edge_1RB_Right	20.27	PC2	PASS
N78-3700 -3800	30	40	CP-QPSK	L	Outer_Full	21.36	PC2	PASS
N78-3700 -3800	30	40	CP-QPSK	L	Inner_Full	22.48	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	M	Edge_1RB_Left	20.22	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	M	Edge_1RB_Right	20.06	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	M	Outer_Full	20.53	PC2	PASS
N78-3700	30	40	DFT-QPSK	M	Inner_Full	20.37	PC2	PASS

-3800								
N78-3700 -3800	30	40	CP-QPSK	M	Edge_1RB_Left	20.28	PC2	PASS
N78-3700 -3800	30	40	CP-QPSK	M	Edge_1RB_Right	20.16	PC2	PASS
N78-3700 -3800	30	40	CP-QPSK	M	Outer_Full	20.77	PC2	PASS
N78-3700 -3800	30	40	CP-QPSK	M	Inner_Full	20.76	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Edge_1RB_Left	19.01	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Edge_1RB_Right	19.09	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Outer_Full	21.95	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Inner_Full	20.72	PC2	PASS
N78-3700 -3800	30	40	CP-QPSK	H	Edge_1RB_Left	19.01	PC2	PASS
N78-3700 -3800	30	40	CP-QPSK	H	Edge_1RB_Right	20.02	PC2	PASS
N78-3700 -3800	30	40	CP-QPSK	H	Outer_Full	22.06	PC2	PASS
N78-3700 -3800	30	40	CP-QPSK	H	Inner_Full	21.06	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Edge_1RB_Left	20.13	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Edge_1RB_Right	21.17	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Outer_Full	21.52	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Inner_Full	21.51	PC2	PASS
N78-3700 -3800	30	50	CP-QPSK	L	Edge_1RB_Left	21.20	PC2	PASS
N78-3700 -3800	30	50	CP-QPSK	L	Edge_1RB_Right	21.21	PC2	PASS
N78-3700 -3800	30	50	CP-QPSK	L	Outer_Full	21.06	PC2	PASS
N78-3700 -3800	30	50	CP-QPSK	L	Inner_Full	21.78	PC2	PASS
N78-3700	30	50	DFT-QPSK	M	Edge_1RB_Left	21.24	PC2	PASS

-3800								
N78-3700 -3800	30	50	DFT-QPSK	M	Edge_1RB_Right	21.15	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	M	Outer_Full	21.84	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	M	Inner_Full	21.86	PC2	PASS
N78-3700 -3800	30	50	CP-QPSK	M	Edge_1RB_Left	21.24	PC2	PASS
N78-3700 -3800	30	50	CP-QPSK	M	Edge_1RB_Right	20.76	PC2	PASS
N78-3700 -3800	30	50	CP-QPSK	M	Outer_Full	19.01	PC2	PASS
N78-3700 -3800	30	50	CP-QPSK	M	Inner_Full	19.09	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	H	Edge_1RB_Left	21.95	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	H	Edge_1RB_Right	20.72	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	H	Outer_Full	19.01	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	H	Inner_Full	20.02	PC2	PASS
N78-3700 -3800	30	50	CP-QPSK	H	Edge_1RB_Left	22.06	PC2	PASS
N78-3700 -3800	30	50	CP-QPSK	H	Edge_1RB_Right	21.06	PC2	PASS
N78-3700 -3800	30	50	CP-QPSK	H	Outer_Full	20.13	PC2	PASS
N78-3700 -3800	30	50	CP-QPSK	H	Inner_Full	21.17	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	L	Edge_1RB_Left	21.52	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	L	Edge_1RB_Right	21.51	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	L	Outer_Full	21.20	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	L	Inner_Full	21.21	PC2	PASS
N78-3700 -3800	30	60	CP-QPSK	L	Edge_1RB_Left	21.06	PC2	PASS
N78-3700	30	60	CP-QPSK	L	Edge_1RB_Right	21.78	PC2	PASS

-3800								
N78-3700 -3800	30	60	CP-QPSK	L	Outer_Full	21.24	PC2	PASS
N78-3700 -3800	30	60	CP-QPSK	L	Inner_Full	21.15	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	M	Edge_1RB_Left	21.84	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	M	Edge_1RB_Right	21.86	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	M	Outer_Full	21.24	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	M	Inner_Full	20.76	PC2	PASS
N78-3700 -3800	30	60	CP-QPSK	M	Edge_1RB_Left	19.01	PC2	PASS
N78-3700 -3800	30	60	CP-QPSK	M	Edge_1RB_Right	19.09	PC2	PASS
N78-3700 -3800	30	60	CP-QPSK	M	Outer_Full	21.95	PC2	PASS
N78-3700 -3800	30	60	CP-QPSK	M	Inner_Full	20.72	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Edge_1RB_Left	19.01	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Edge_1RB_Right	20.02	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Outer_Full	22.06	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Inner_Full	21.06	PC2	PASS
N78-3700 -3800	30	60	CP-QPSK	H	Edge_1RB_Left	20.13	PC2	PASS
N78-3700 -3800	30	60	CP-QPSK	H	Edge_1RB_Right	21.17	PC2	PASS
N78-3700 -3800	30	60	CP-QPSK	H	Outer_Full	21.52	PC2	PASS
N78-3700 -3800	30	60	CP-QPSK	H	Inner_Full	21.51	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	L	Edge_1RB_Left	21.20	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	L	Edge_1RB_Right	21.21	PC2	PASS
N78-3700	30	80	DFT-QPSK	L	Outer_Full	21.06	PC2	PASS

-3800								
N78-3700 -3800	30	80	DFT-QPSK	L	Inner_Full	21.78	PC2	PASS
N78-3700 -3800	30	80	CP-QPSK	L	Edge_1RB_Left	21.24	PC2	PASS
N78-3700 -3800	30	80	CP-QPSK	L	Edge_1RB_Right	21.15	PC2	PASS
N78-3700 -3800	30	80	CP-QPSK	L	Outer_Full	21.84	PC2	PASS
N78-3700 -3800	30	80	CP-QPSK	L	Inner_Full	21.86	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Edge_1RB_Left	21.24	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Edge_1RB_Right	21.48	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Outer_Full	21.82	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Inner_Full	21.10	PC2	PASS
N78-3700 -3800	30	80	CP-QPSK	M	Edge_1RB_Left	21.33	PC2	PASS
N78-3700 -3800	30	80	CP-QPSK	M	Edge_1RB_Right	21.11	PC2	PASS
N78-3700 -3800	30	80	CP-QPSK	M	Outer_Full	21.86	PC2	PASS
N78-3700 -3800	30	80	CP-QPSK	M	Inner_Full	21.01	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	H	Edge_1RB_Left	21.10	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	H	Edge_1RB_Right	21.11	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	H	Outer_Full	21.29	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	H	Inner_Full	21.59	PC2	PASS
N78-3700 -3800	30	80	CP-QPSK	H	Edge_1RB_Left	21.03	PC2	PASS
N78-3700 -3800	30	80	CP-QPSK	H	Edge_1RB_Right	21.17	PC2	PASS
N78-3700 -3800	30	80	CP-QPSK	H	Outer_Full	21.52	PC2	PASS
N78-3700	30	80	CP-QPSK	H	Inner_Full	21.39	PC2	PASS

-3800								
N78-3700 -3800	30	90	DFT-QPSK	L	Edge_1RB_Left	21.35	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	L	Edge_1RB_Right	21.37	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	L	Outer_Full	21.37	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	L	Inner_Full	21.57	PC2	PASS
N78-3700 -3800	30	90	CP-QPSK	L	Edge_1RB_Left	21.42	PC2	PASS
N78-3700 -3800	30	90	CP-QPSK	L	Edge_1RB_Right	21.37	PC2	PASS
N78-3700 -3800	30	90	CP-QPSK	L	Outer_Full	21.44	PC2	PASS
N78-3700 -3800	30	90	CP-QPSK	L	Inner_Full	22.65	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Edge_1RB_Left	21.44	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Edge_1RB_Right	21.32	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Outer_Full	21.44	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Inner_Full	21.63	PC2	PASS
N78-3700 -3800	30	90	CP-QPSK	M	Edge_1RB_Left	21.43	PC2	PASS
N78-3700 -3800	30	90	CP-QPSK	M	Edge_1RB_Right	21.41	PC2	PASS
N78-3700 -3800	30	90	CP-QPSK	M	Outer_Full	21.59	PC2	PASS
N78-3700 -3800	30	90	CP-QPSK	M	Inner_Full	21.74	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	H	Edge_1RB_Left	21.44	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	H	Edge_1RB_Right	21.14	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	H	Outer_Full	21.54	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	H	Inner_Full	21.68	PC2	PASS
N78-3700	30	90	CP-QPSK	H	Edge_1RB_Left	21.46	PC2	PASS

-3800								
N78-3700 -3800	30	90	CP-QPSK	H	Edge_1RB_Right	21.19	PC2	PASS
N78-3700 -3800	30	90	CP-QPSK	H	Outer_Full	21.69	PC2	PASS
N78-3700 -3800	30	90	CP-QPSK	H	Inner_Full	21.81	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Edge_1RB_Left	22.05	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Edge_1RB_Right	21.81	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Outer_Full	22.73	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Inner_Full	21.89	PC2	PASS
N78-3700 -3800	30	100	CP-QPSK	L	Edge_1RB_Left	22.03	PC2	PASS
N78-3700 -3800	30	100	CP-QPSK	L	Edge_1RB_Right	21.90	PC2	PASS
N78-3700 -3800	30	100	CP-QPSK	L	Outer_Full	22.73	PC2	PASS
N78-3700 -3800	30	100	CP-QPSK	L	Inner_Full	24.68	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Edge_1RB_Left	19.74	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Edge_1RB_Right	21.73	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Outer_Full	23.61	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Inner_Full	24.91	PC2	PASS
N78-3700 -3800	30	100	CP-QPSK	M	Edge_1RB_Left	19.81	PC2	PASS
N78-3700 -3800	30	100	CP-QPSK	M	Edge_1RB_Right	21.66	PC2	PASS
N78-3700 -3800	30	100	CP-QPSK	M	Outer_Full	21.68	PC2	PASS
N78-3700 -3800	30	100	CP-QPSK	M	Inner_Full	23.29	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	H	Edge_1RB_Left	19.77	PC2	PASS
N78-3700	30	100	DFT-QPSK	H	Edge_1RB_Right	21.63	PC2	PASS

-3800								
N78-3700 -3800	30	100	DFT-QPSK	H	Outer_Full	23.72	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	H	Inner_Full	24.83	PC2	PASS
N78-3700 -3800	30	100	CP-QPSK	H	Edge_1RB_Left	19.69	PC2	PASS
N78-3700 -3800	30	100	CP-QPSK	H	Edge_1RB_Right	21.67	PC2	PASS
N78-3700 -3800	30	100	CP-QPSK	H	Outer_Full	21.70	PC2	PASS
N78-3700 -3800	30	100	CP-QPSK	H	Inner_Full	23.29	PC2	PASS

Appendix B: Peak-to-Average Ratio for SA

Peak-to-Average Ratio(CCDF)

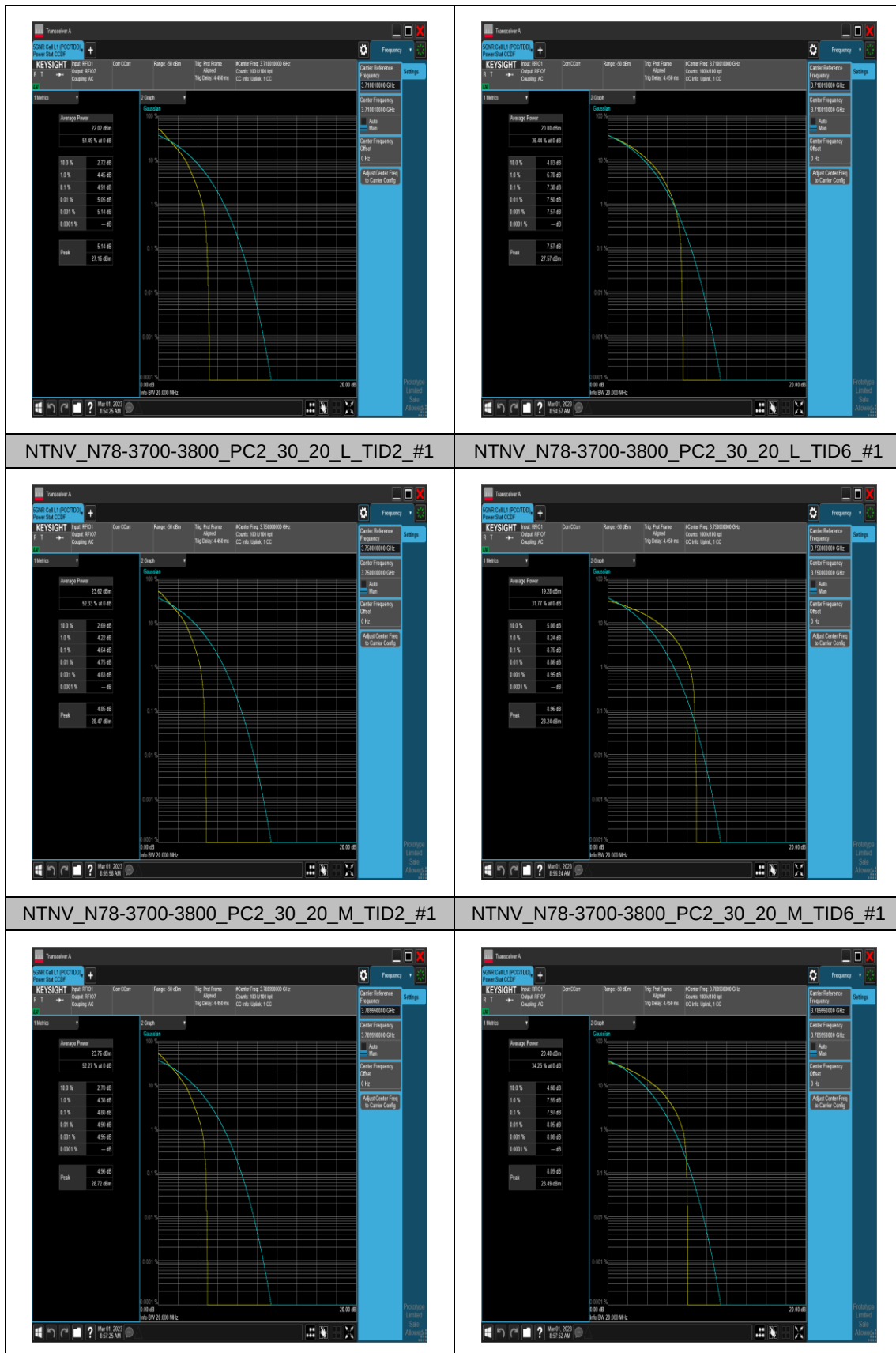
Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
N78-3700-3800	30	20	DFT-QPSK	L	Outer_Full	4.91	≤13	PASS
N78-3700-3800	30	20	CP-QPSK	L	Outer_Full	7.38	≤13	PASS
N78-3700-3800	30	20	DFT-QPSK	M	Outer_Full	4.64	≤13	PASS
N78-3700-3800	30	20	CP-QPSK	M	Outer_Full	8.76	≤13	PASS
N78-3700-3800	30	20	DFT-QPSK	H	Outer_Full	4.80	≤13	PASS
N78-3700-3800	30	20	CP-QPSK	H	Outer_Full	7.97	≤13	PASS
N78-3700-3800	30	40	DFT-QPSK	L	Outer_Full	5.04	≤13	PASS
N78-3700-3800	30	40	CP-QPSK	L	Outer_Full	6.88	≤13	PASS
N78-3700-3800	30	40	DFT-QPSK	M	Outer_Full	4.68	≤13	PASS
N78-3700-3800	30	40	CP-QPSK	M	Outer_Full	6.82	≤13	PASS
N78-3700-3800	30	40	DFT-QPSK	H	Outer_Full	4.80	≤13	PASS
N78-3700-3800	30	40	CP-QPSK	H	Outer_Full	6.95	≤13	PASS
N78-3700-3800	30	50	DFT-QPSK	L	Outer_Full	5.28	≤13	PASS
N78-3700-3800	30	50	CP-QPSK	L	Outer_Full	7.09	≤13	PASS
N78-3700-3800	30	50	DFT-QPSK	M	Outer_Full	4.86	≤13	PASS
N78-3700-3800	30	50	CP-QPSK	M	Outer_Full	6.82	≤13	PASS

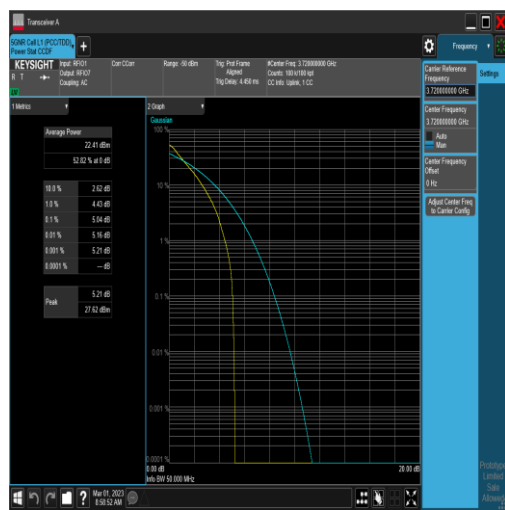
N78-3700-3800	30	50	DFT-QPSK	H	Outer_Full	4.84	≤13	PASS
N78-3700-3800	30	50	CP-QPSK	H	Outer_Full	6.90	≤13	PASS
N78-3700-3800	30	60	DFT-QPSK	L	Outer_Full	5.01	≤13	PASS
N78-3700-3800	30	60	CP-QPSK	L	Outer_Full	6.90	≤13	PASS
N78-3700-3800	30	60	DFT-QPSK	M	Outer_Full	4.69	≤13	PASS
N78-3700-3800	30	60	CP-QPSK	M	Outer_Full	6.83	≤13	PASS
N78-3700-3800	30	60	DFT-QPSK	H	Outer_Full	4.80	≤13	PASS
N78-3700-3800	30	60	CP-QPSK	H	Outer_Full	6.94	≤13	PASS
N78-3700-3800	30	80	DFT-QPSK	L	Outer_Full	5.11	≤13	PASS
N78-3700-3800	30	80	CP-QPSK	L	Outer_Full	6.99	≤13	PASS
N78-3700-3800	30	80	DFT-QPSK	M	Outer_Full	4.97	≤13	PASS
N78-3700-3800	30	80	CP-QPSK	M	Outer_Full	6.85	≤13	PASS
N78-3700-3800	30	80	DFT-QPSK	H	Outer_Full	4.93	≤13	PASS
N78-3700-3800	30	80	CP-QPSK	H	Outer_Full	6.90	≤13	PASS
N78-3700-3800	30	90	DFT-QPSK	L	Outer_Full	5.07	≤13	PASS
N78-3700-3800	30	90	CP-QPSK	L	Outer_Full	6.94	≤13	PASS
N78-3700-3800	30	90	DFT-QPSK	M	Outer_Full	5.02	≤13	PASS
N78-3700-3800	30	90	CP-QPSK	M	Outer_Full	6.91	≤13	PASS
N78-3700-3800	30	90	DFT-QPSK	H	Outer_Full	4.96	≤13	PASS
N78-3700-3800	30	90	CP-QPSK	H	Outer_Full	6.96	≤13	PASS
N78-3700-3800	30	100	DFT-QPSK	L	Outer_Full	5.09	≤13	PASS

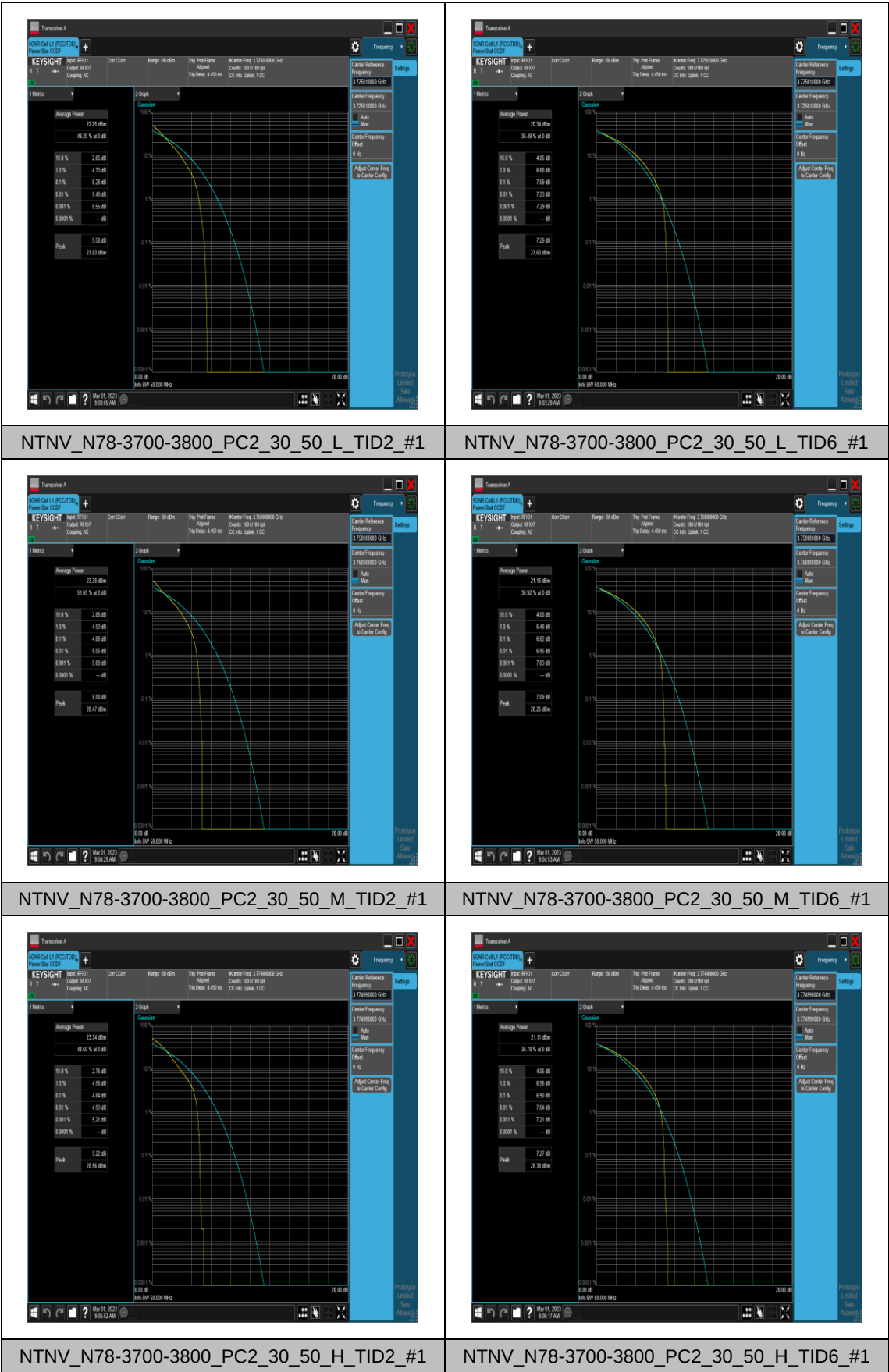
N78-3700-3800	30	100	CP-QPSK	L	Outer_Full	6.96	≤13	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_Full	8.31	≤13	PASS
N78-3700-3800	30	100	CP-QPSK	M	Outer_Full	8.45	≤13	PASS
N78-3700-3800	30	100	DFT-QPSK	H	Outer_Full	8.32	≤13	PASS
N78-3700-3800	30	100	CP-QPSK	H	Outer_Full	8.44	≤13	PASS

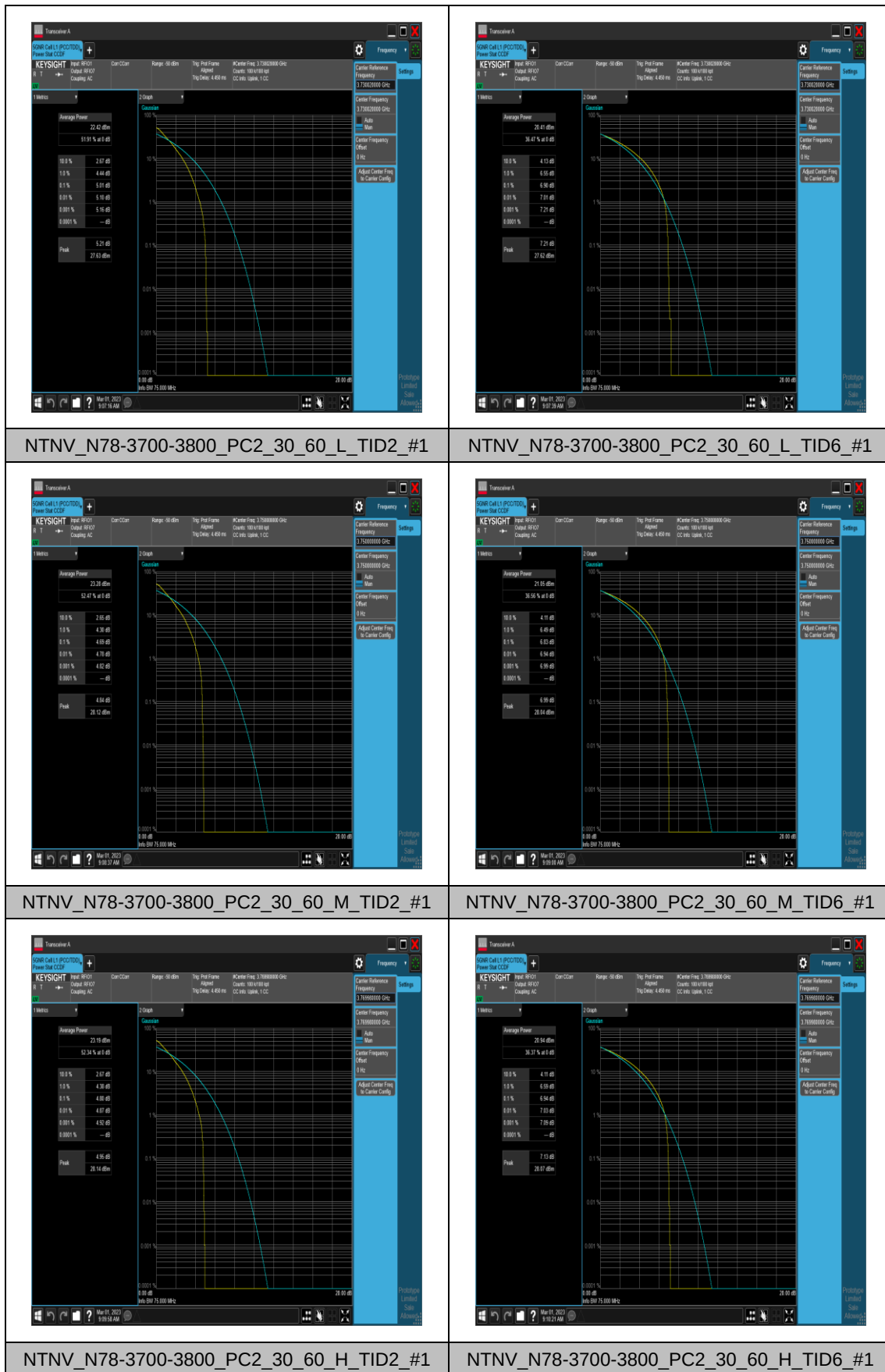
Test Graphs

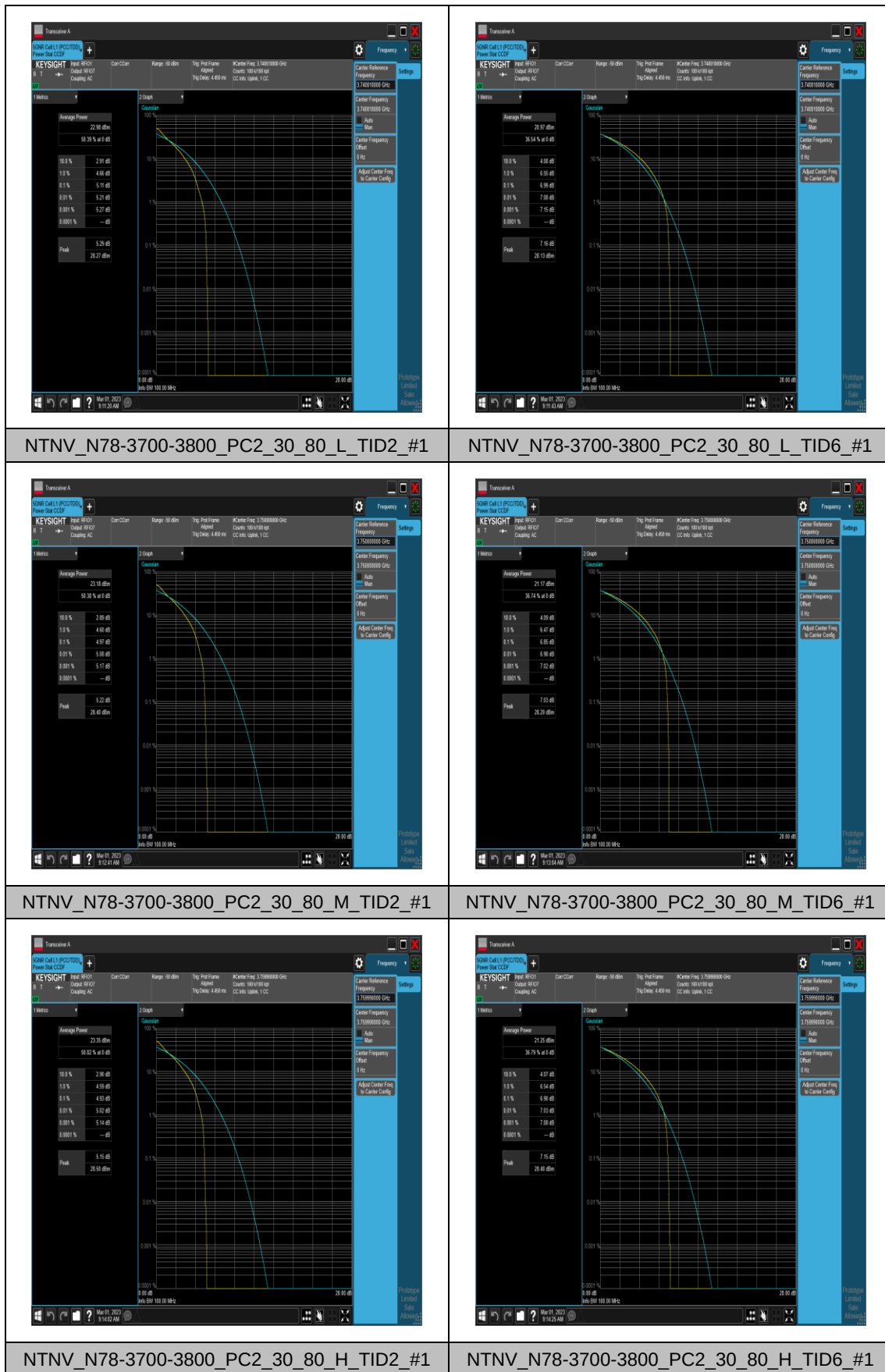


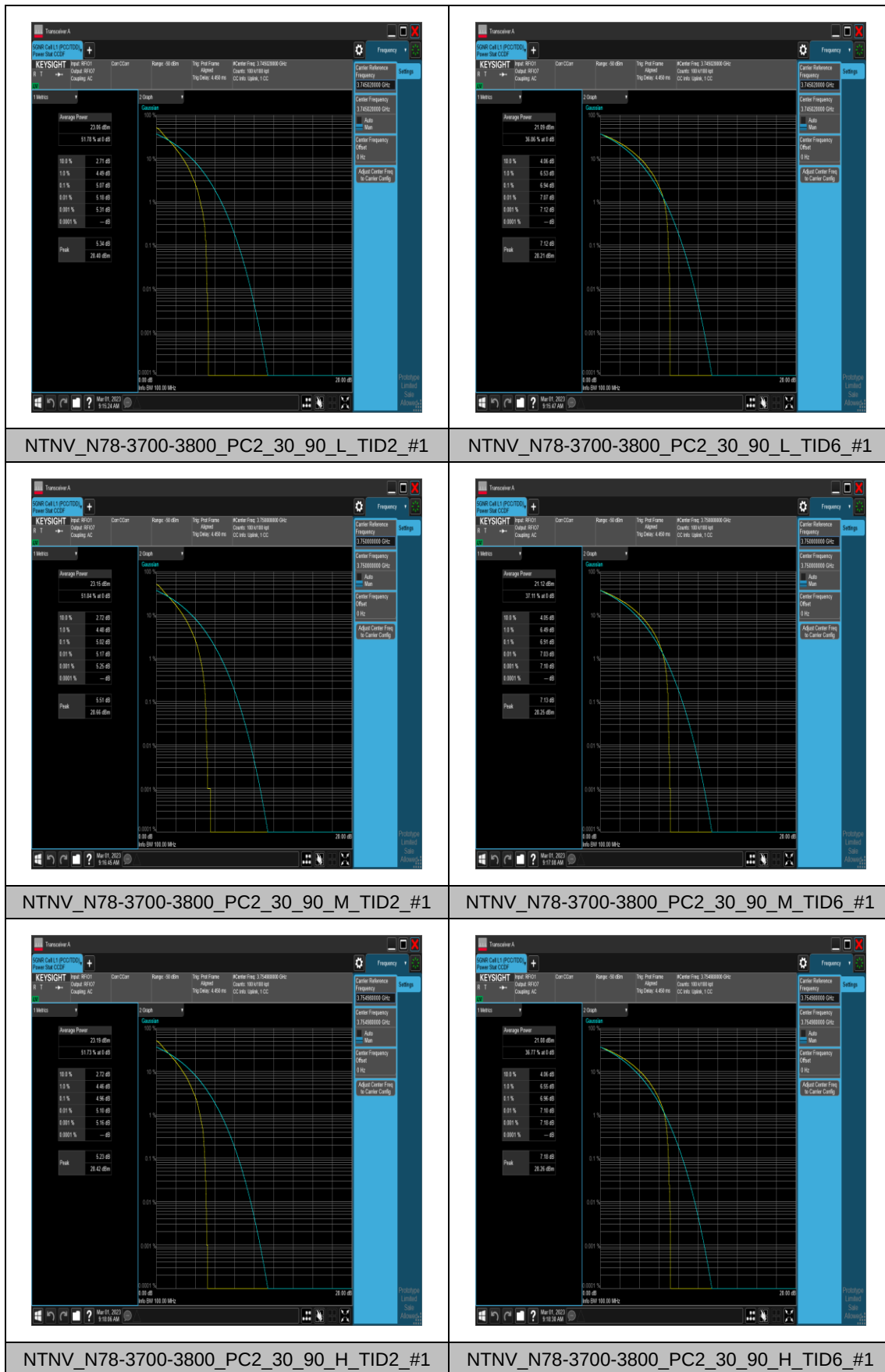
NTNV_N78-3700-3800_PC2_30_20_H_TID2_#1

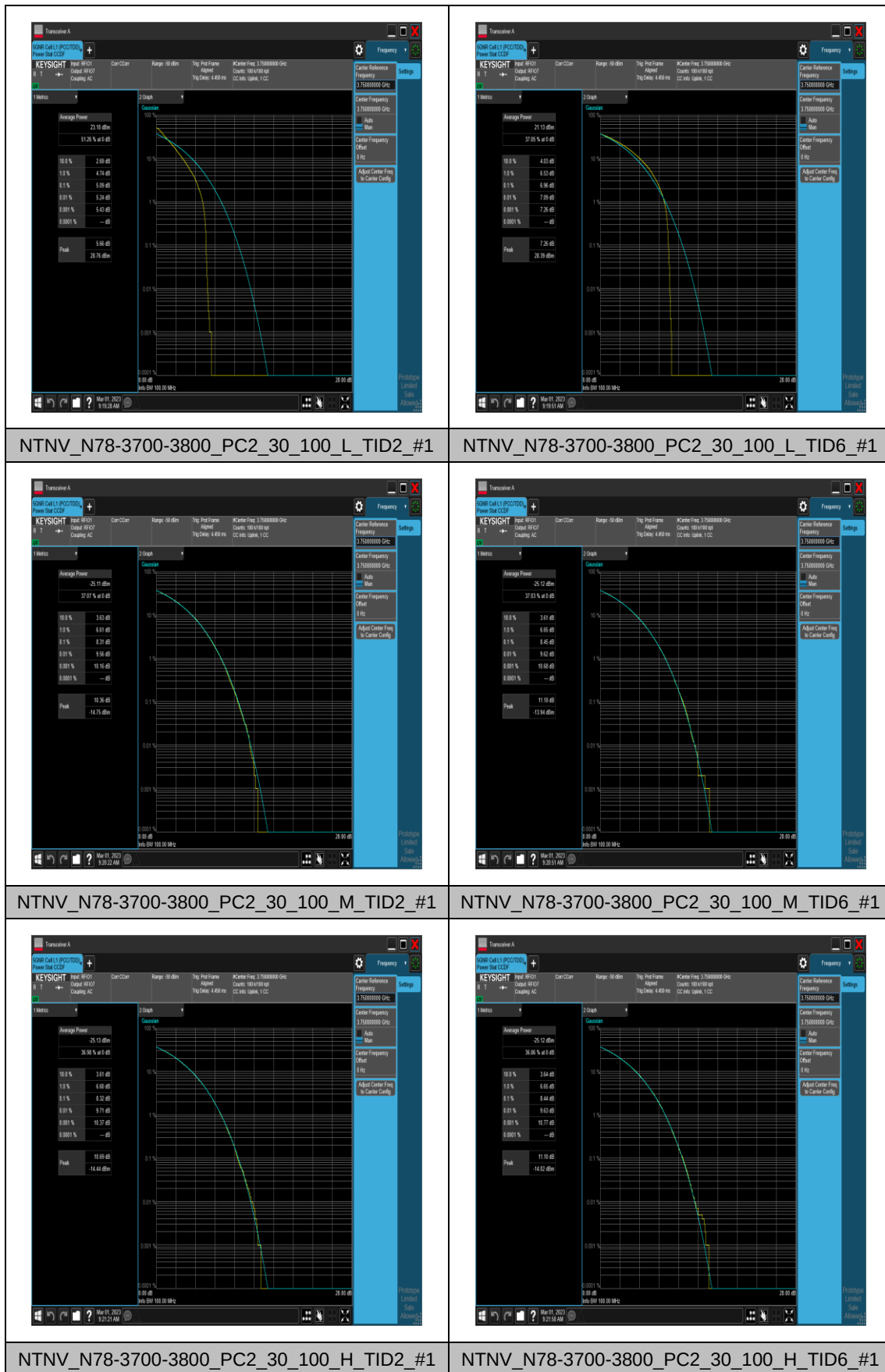












Appendix C: 26dB Bandwidth and Occupied Bandwidth for SA

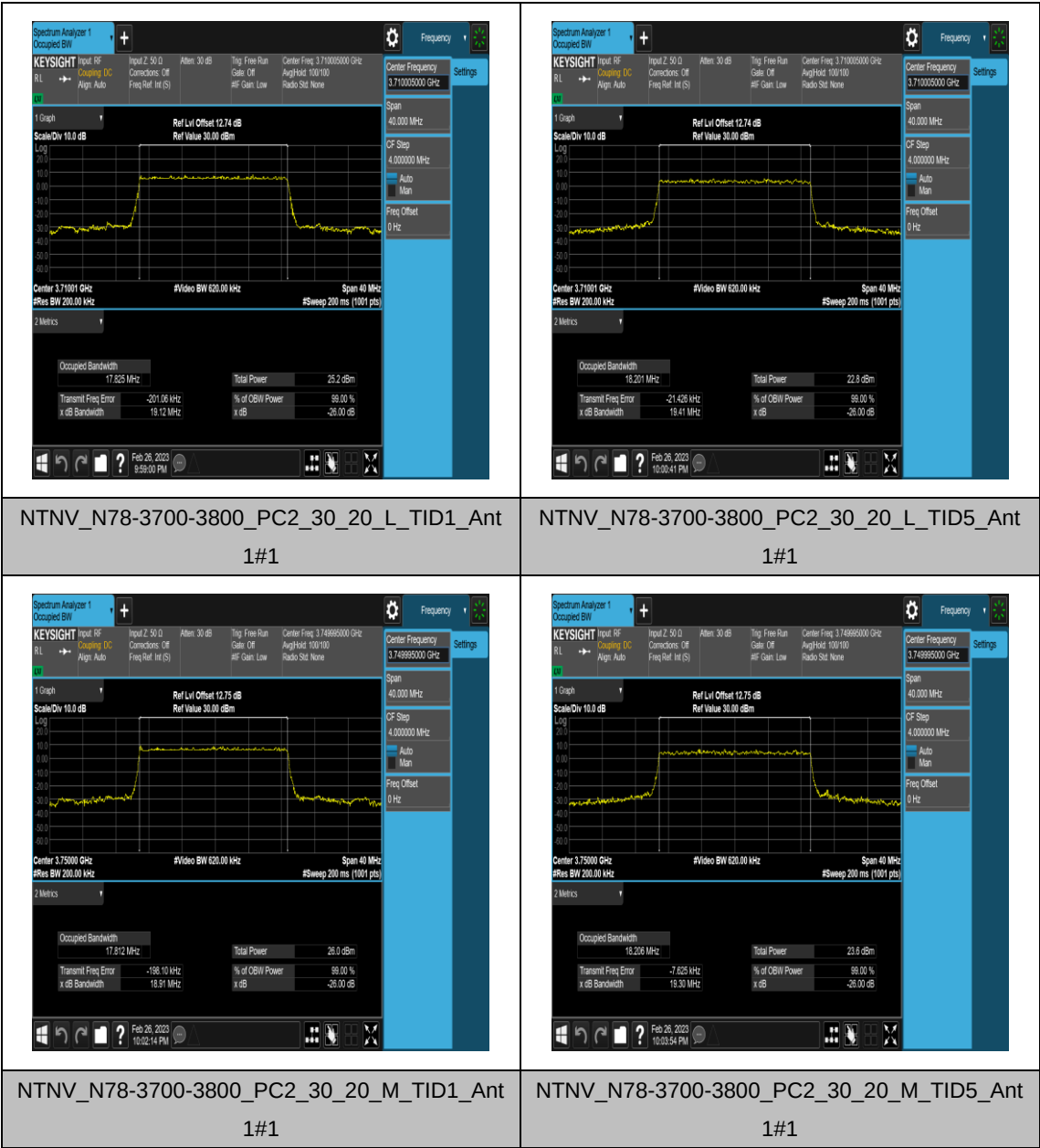
Test Result

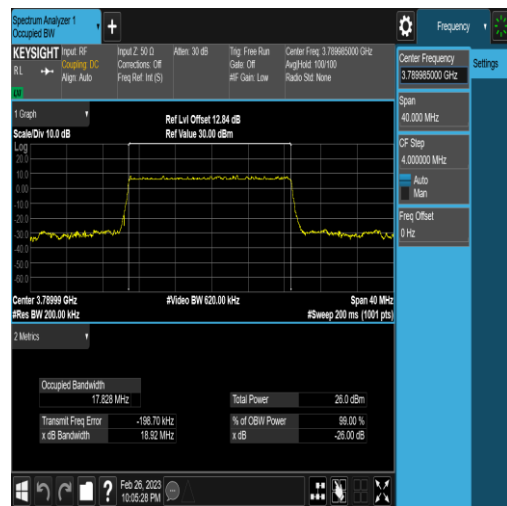
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
N78-3700 -3800	30	20	DFT-QPSK	L	Outer_Full	17.82	19.12	PASS
N78-3700 -3800	30	20	CP-QPSK	L	Outer_Full	18.201	19.41	PASS
N78-3700 -3800	30	20	DFT-QPSK	M	Outer_Full	17.812	18.91	PASS
N78-3700 -3800	30	20	CP-QPSK	M	Outer_Full	18.206	19.30	PASS
N78-3700 -3800	30	20	DFT-QPSK	H	Outer_Full	17.828	18.92	PASS
N78-3700 -3800	30	20	CP-QPSK	H	Outer_Full	18.226	19.42	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Outer_Full	35.701	37.19	PASS
N78-3700 -3800	30	40	CP-QPSK	L	Outer_Full	37.817	39.65	PASS
N78-3700 -3800	30	40	DFT-QPSK	M	Outer_Full	35.664	37.34	PASS
N78-3700 -3800	30	40	CP-QPSK	M	Outer_Full	37.816	39.45	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Outer_Full	35.710	37.33	PASS
N78-3700 -3800	30	40	CP-QPSK	H	Outer_Full	37.816	39.61	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Outer_Full	45.720	47.66	PASS
N78-3700 -3800	30	50	CP-QPSK	L	Outer_Full	47.466	49.51	PASS
N78-3700 -3800	30	50	DFT-QPSK	M	Outer_Full	45.746	47.72	PASS
N78-3700 -3800	30	50	CP-QPSK	M	Outer_Full	47.381	49.65	PASS
N78-3700 -3800	30	50	DFT-QPSK	H	Outer_Full	45.681	47.59	PASS
N78-3700 -3800	30	50	CP-QPSK	H	Outer_Full	47.456	49.47	PASS

N78-3700 -3800	30	60	DFT-QPSK	L	Outer_Full	57.734	59.95	PASS
N78-3700 -3800	30	60	CP-QPSK	L	Outer_Full	57.755	60.22	PASS
N78-3700 -3800	30	60	CP-QPSK	M	Outer_Full	57.608	60.07	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Outer_Full	57.787	60.25	PASS
N78-3700 -3800	30	60	CP-QPSK	H	Outer_Full	57.774	60.10	PASS
N78-3700 -3800	30	80	DFT-QPSK	L	Outer_Full	76.731	79.95	PASS
N78-3700 -3800	30	80	CP-QPSK	L	Outer_Full	77.496	80.17	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Outer_Full	76.936	79.94	PASS
N78-3700 -3800	30	80	CP-QPSK	M	Outer_Full	77.522	80.07	PASS
N78-3700 -3800	30	80	DFT-QPSK	H	Outer_Full	76.917	79.83	PASS
N78-3700 -3800	30	80	CP-QPSK	H	Outer_Full	77.512	80.23	PASS
N78-3700 -3800	30	90	DFT-QPSK	L	Outer_Full	86.516	89.61	PASS
N78-3700 -3800	30	90	CP-QPSK	L	Outer_Full	87.331	90.54	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Outer_Full	86.776	89.59	PASS
N78-3700 -3800	30	90	CP-QPSK	M	Outer_Full	87.371	90.51	PASS
N78-3700 -3800	30	90	DFT-QPSK	H	Outer_Full	86.638	89.74	PASS
N78-3700 -3800	30	90	CP-QPSK	H	Outer_Full	87.473	90.69	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Outer_Full	96.409	99.71	PASS
N78-3700 -3800	30	100	CP-QPSK	L	Outer_Full	97.469	100.8	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Outer_Full	96.291	99.71	PASS
N78-3700 -3800	30	100	CP-QPSK	M	Outer_Full	97.386	100.8	PASS

N78-3700 -3800	30	100	DFT-QPSK	H	Outer_Full	96.381	99.61	PASS
N78-3700 -3800	30	100	CP-QPSK	H	Outer_Full	97.423	100.9	PASS

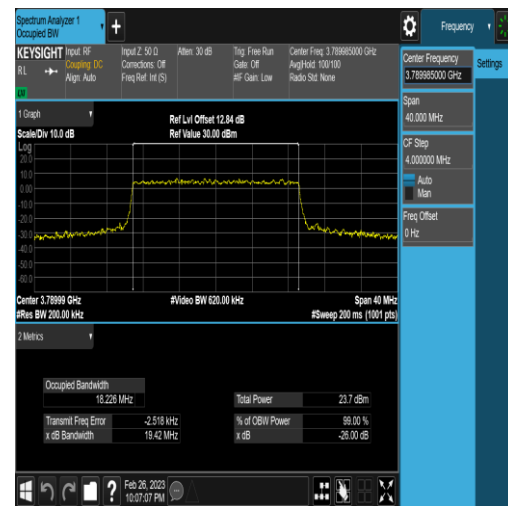
Test Graphs





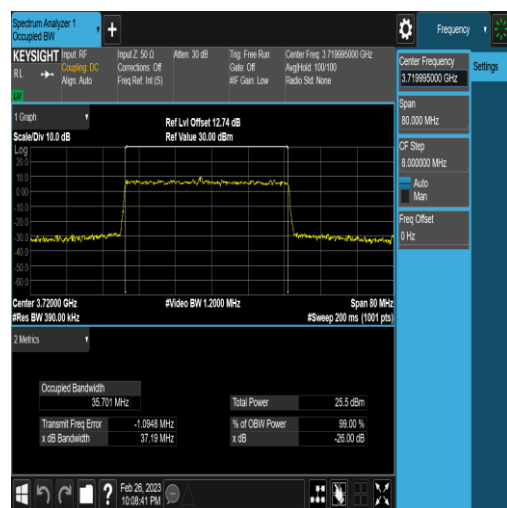
NTNV_N78-3700-3800_PC2_30_20_H_TID1_Ant

1#1



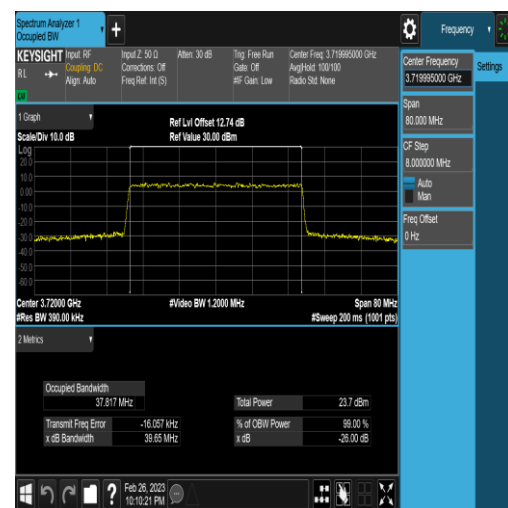
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1#1



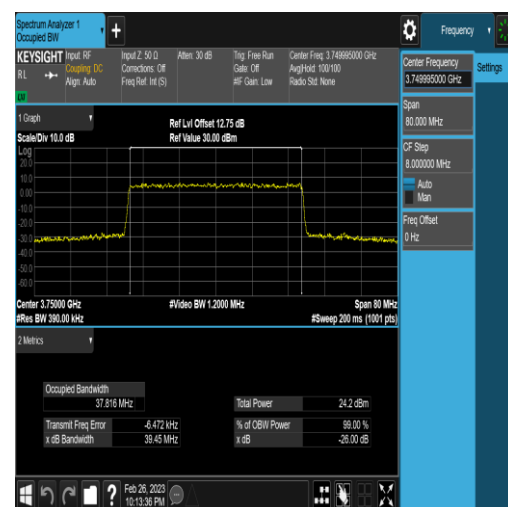
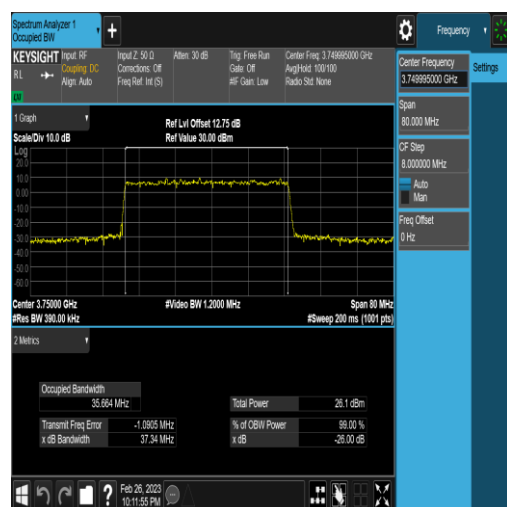
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1#1

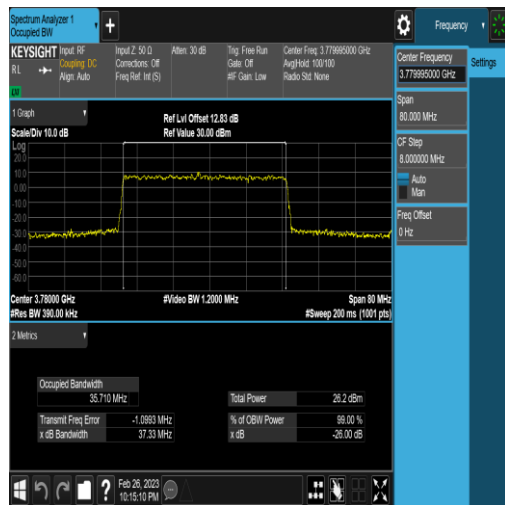


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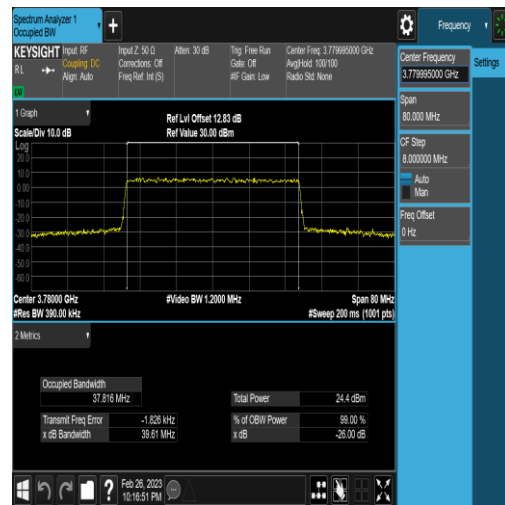
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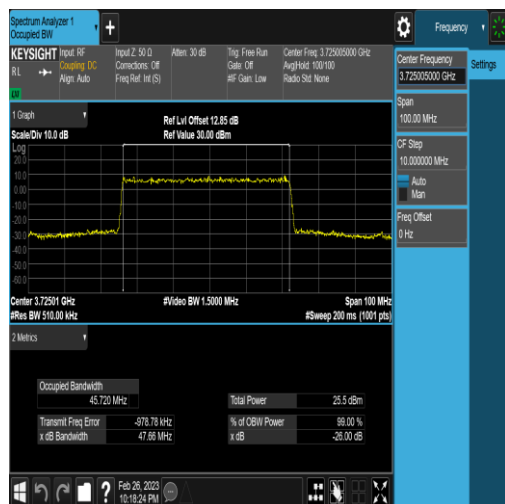
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1#1



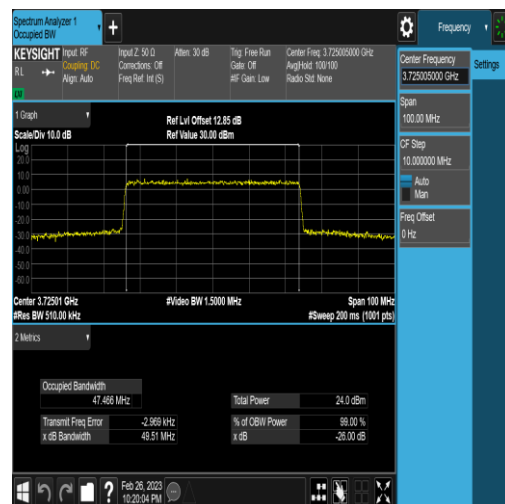
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1#1



NTNV_N78-3700-3800_PC2_30_40_H_TID1_Ant
1#1



NTNV_N78-3700-3800_PC2_30_40_H_TID5_Ant
1#1



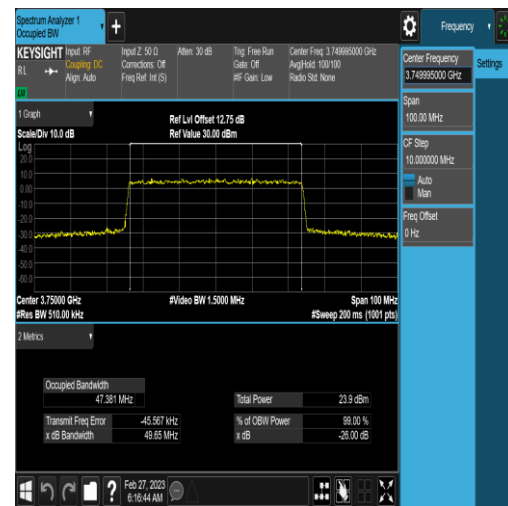
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1#1

NTNV_N78-3700-3800_PC2_30_50_L_TID5_Ant
1#1



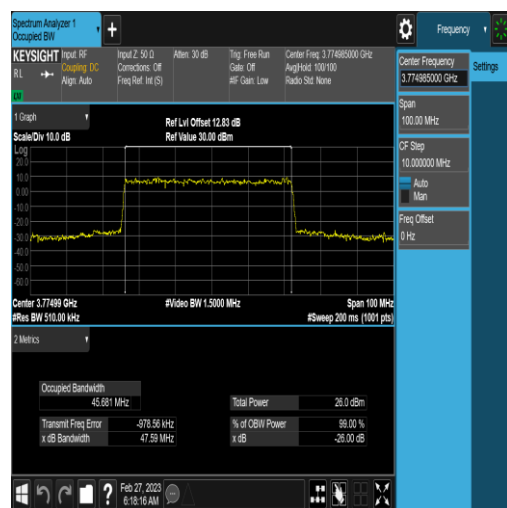
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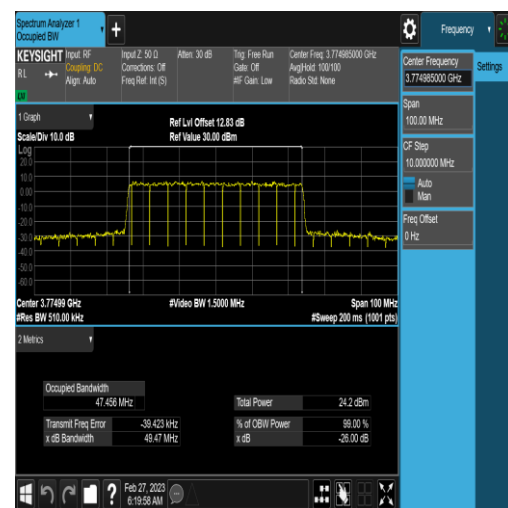
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1#1



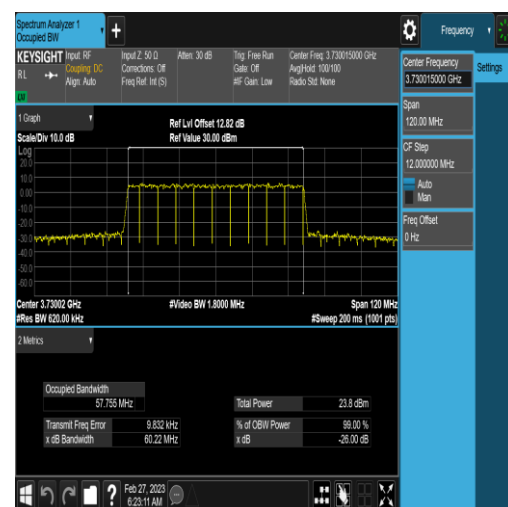
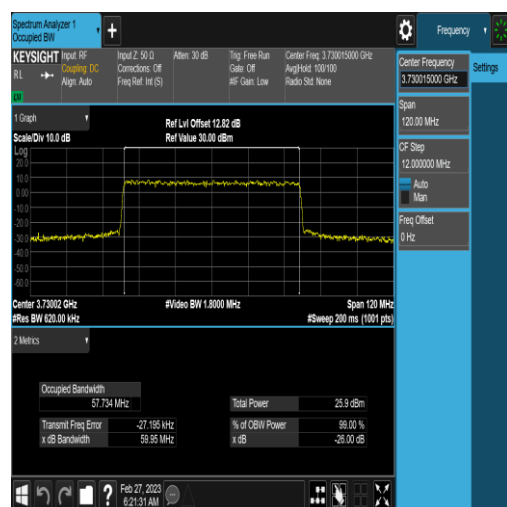
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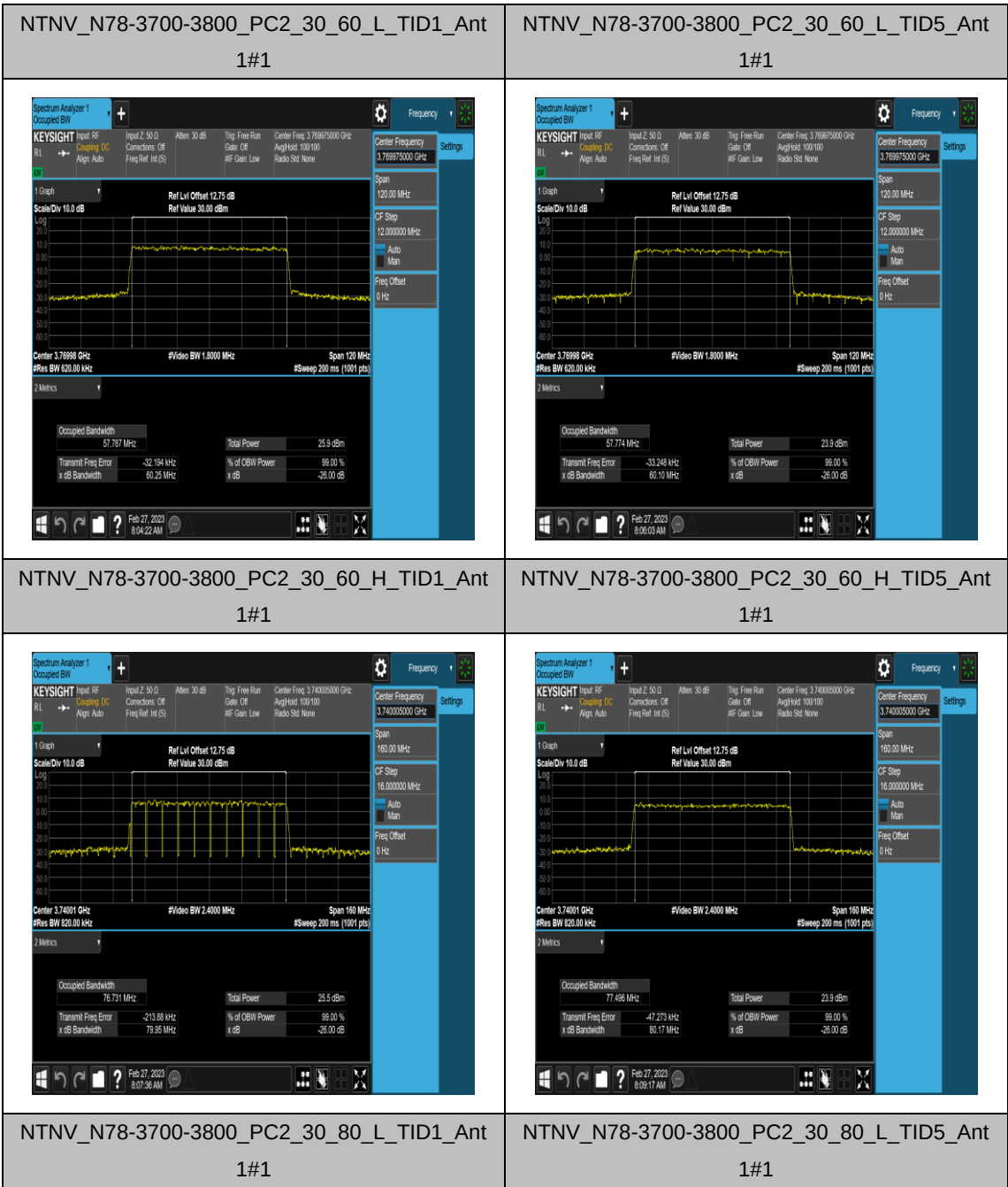
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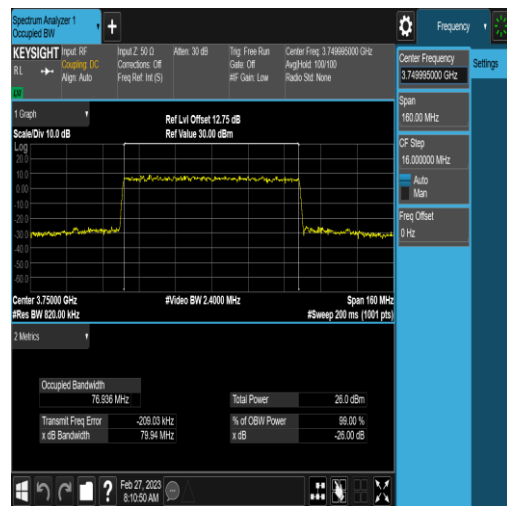


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1#1

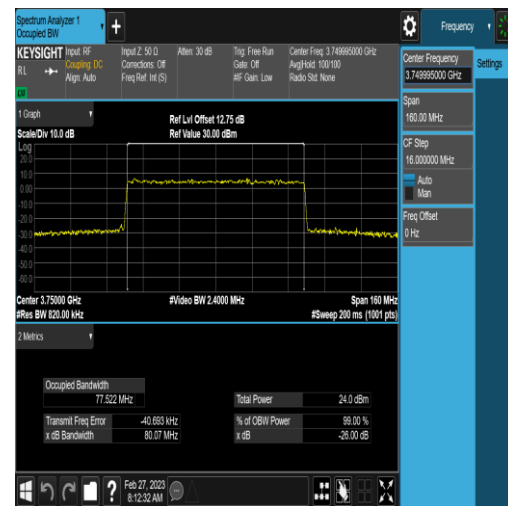






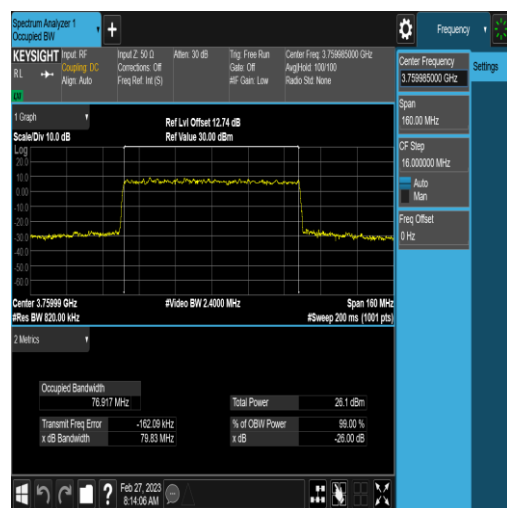
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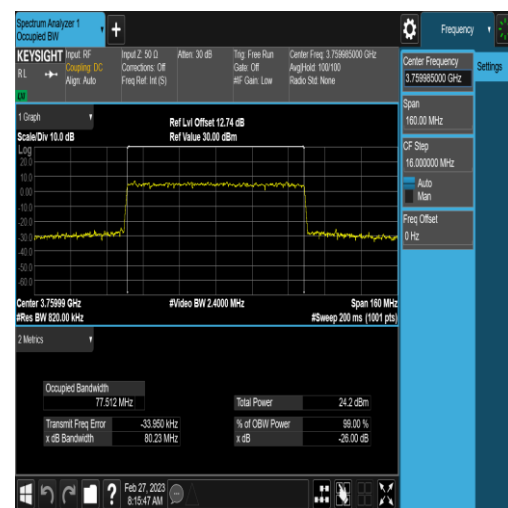
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1#1



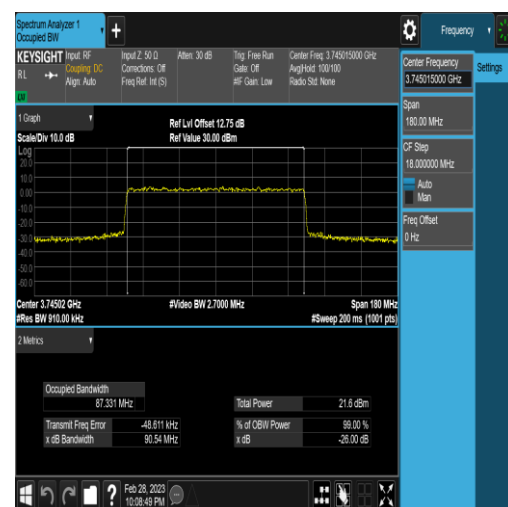
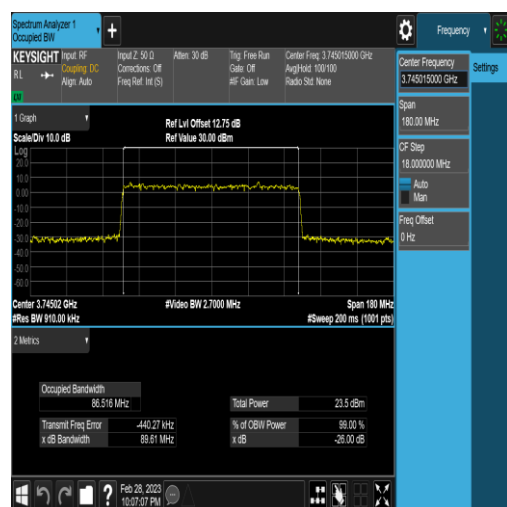
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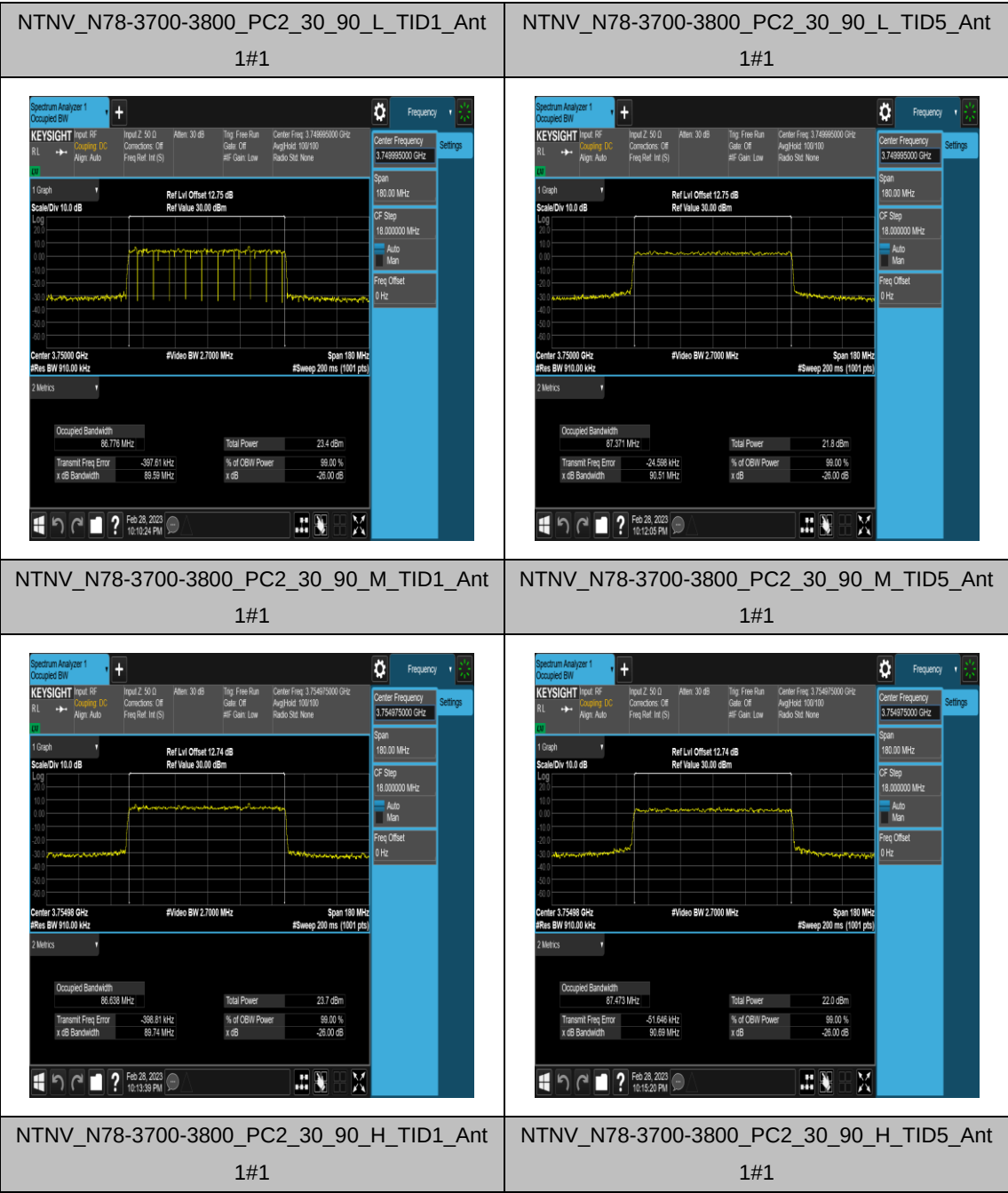
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NTNV_N78-3700-3800_PC2_30_80_H_TID5_Ant

1#1

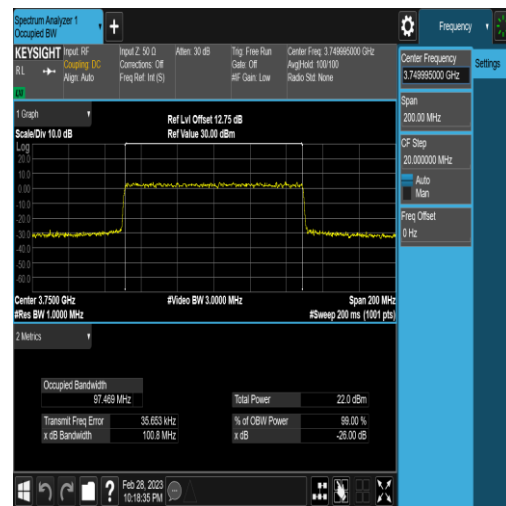






NTNV_N78-3700-3800_PC2_30_100_L_TID1_An

t1#1



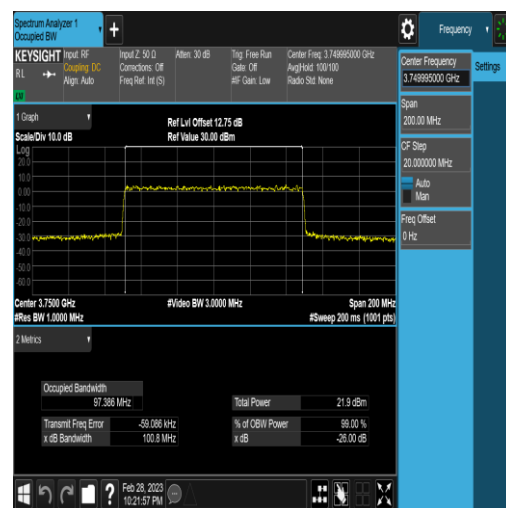
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t1#1



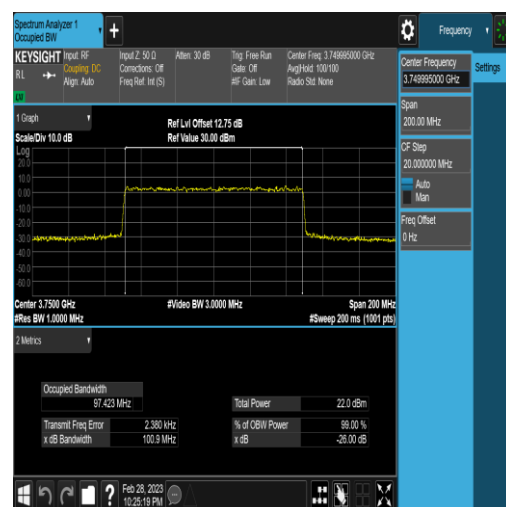
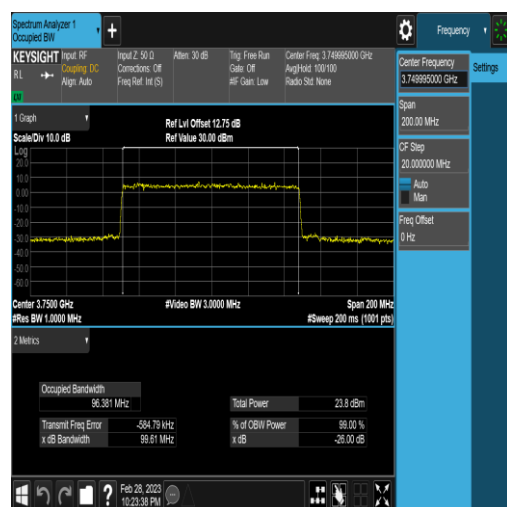
NTNV_N78-3700-3800_PC2_30_100_M_TID1_A

nt1#1



NTNV_N78-3700-3800_PC2_30_100_M_TID5_A

nt1#1



NTNV_N78-3700-3800_PC2_30_100_H_TID1_An t1#1	NTNV_N78-3700-3800_PC2_30_100_H_TID5_An t1#1
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Appendix D: Band Edge for SA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Verdict
N78-3700-3800	30	20	CP-QPSK	L	Edge_1RB_Left	see graph	PASS
N78-3700-3800	30	20	CP-QPSK	L	Outer_Full	see graph	PASS
N78-3700-3800	30	20	CP-QPSK	H	Edge_1RB_Right	see graph	PASS
N78-3700-3800	30	20	CP-QPSK	H	Outer_Full	see graph	PASS
N78-3700-3800	30	40	CP-QPSK	L	Edge_1RB_Left	see graph	PASS
N78-3700-3800	30	40	CP-QPSK	L	Outer_Full	see graph	PASS
N78-3700-3800	30	40	CP-QPSK	H	Edge_1RB_Right	see graph	PASS
N78-3700-3800	30	40	CP-QPSK	H	Outer_Full	see graph	PASS
N78-3700-3800	30	50	CP-QPSK	L	Edge_1RB_Left	see graph	PASS
N78-3700-3800	30	50	CP-QPSK	L	Outer_Full	see graph	PASS
N78-3700-3800	30	50	CP-QPSK	H	Edge_1RB_Right	see graph	PASS
N78-3700-3800	30	50	CP-QPSK	H	Outer_Full	see graph	PASS
N78-3700-3800	30	60	CP-QPSK	L	Edge_1RB_Left	see graph	PASS
N78-3700-3800	30	60	CP-QPSK	L	Outer_Full	see graph	PASS
N78-3700-3800	30	60	CP-QPSK	H	Edge_1RB_Right	see graph	PASS
N78-3700-3800	30	60	CP-QPSK	H	Outer_Full	see graph	PASS
N78-3700-3800	30	80	CP-QPSK	L	Edge_1RB_Left	see graph	PASS
N78-3700-3800	30	80	CP-QPSK	L	Outer_Full	see graph	PASS
N78-3700-3800	30	80	CP-QPSK	H	Edge_1RB_Right	see graph	PASS

3800							
N78-3700-3800	30	80	CP-QPSK	H	Outer_Full	see graph	PASS
N78-3700-3800	30	90	CP-QPSK	L	Edge_1RB_Left	see graph	PASS
N78-3700-3800	30	90	CP-QPSK	L	Outer_Full	see graph	PASS
N78-3700-3800	30	90	CP-QPSK	H	Edge_1RB_Right	see graph	PASS
N78-3700-3800	30	90	CP-QPSK	H	Outer_Full	see graph	PASS
N78-3700-3800	30	100	CP-QPSK	L	Edge_1RB_Left	see graph	PASS
N78-3700-3800	30	100	CP-QPSK	L	Outer_Full	see graph	PASS
N78-3700-3800	30	100	CP-QPSK	H	Edge_1RB_Right	see graph	PASS
N78-3700-3800	30	100	CP-QPSK	H	Outer_Full	see graph	PASS

Test Graphs

